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(71) Applicant (for all designated States except US): NOKIA NETWORKS OY [FI/FI]; P.O. Box 300, FIN-00045 Nokia Group (FI).

(72) Inventors; and

(75) Inventors/Applicants (for US only): LAHTI, Harri [FI/FI]; Hevontie 25 B, FIN-01820 Klaukkala (FI). TORVINEN, Marko [FI/FI]; Kilonpuistonkatu 3 A 16, FIN-02610 Espoo (FI).

(74) Agent: BERGGREN OY AB; P.O. Box 16, FIN-00101 Helsinki (FI).

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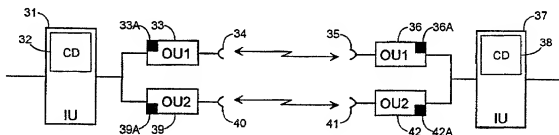
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(54) Title: METHOD AND ARRANGEMENT FOR CHANGING PARALLEL CLOCK SIGNALS IN A DIGITAL DATA TRANSMISSION



## (57) Abstract

A novel changeover arrangement for the clock signals of parallel transmission connections of an assured data transmission link. According to the method of the invention, the transmission path to be received is changed, prior to losing the phase lock, and the data transmission of the link remains free of errors, in case even one of the transmission paths transmits the clock signal as sufficiently free of errors, even if errors occur in the other. This is realised by sending for the transmission paths a clock signal by parallel outdoor units (OU) located in succession to a common indoor unit (IU), by receiving said clock signal by a corresponding set of second outdoor units, where phase locked signals are used for locking to the signal, whereafter a second indoor unit receives information of the mode of the phase lock, as well as by selecting in the receiving indoor unit, on the basis of the mode information obtained from the outdoor unit, a transmission path that has less errors, in case errors are caused in the employed connection. Here also a fading of the clock signal, leading to a disconnection from the phase lock, is considered as an error.